

boot[3:0] chooses SD > eMMC > USB

boot[5:4] chooses XTAL

## CPU: Clock&System

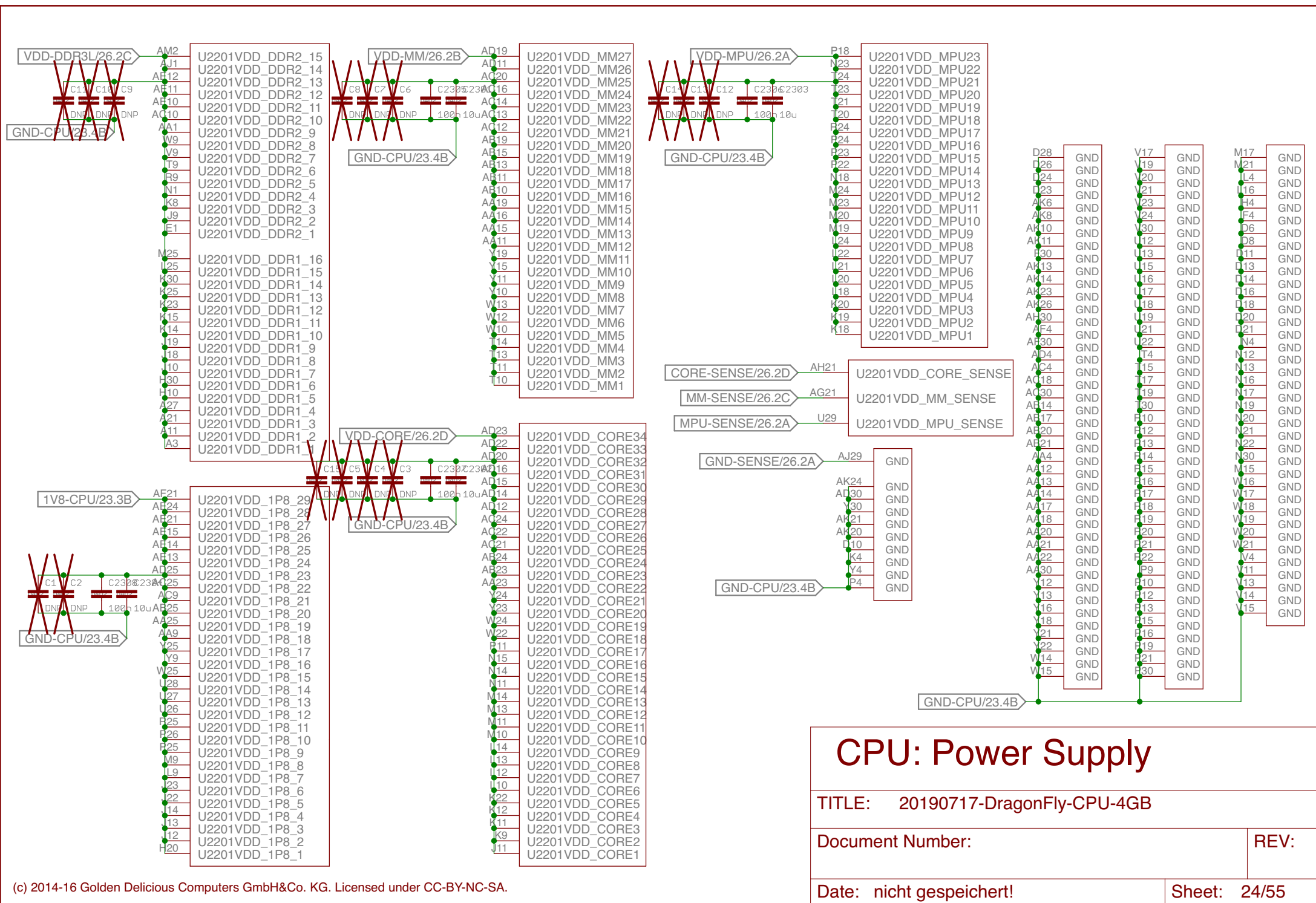
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# CPU: Power Supply

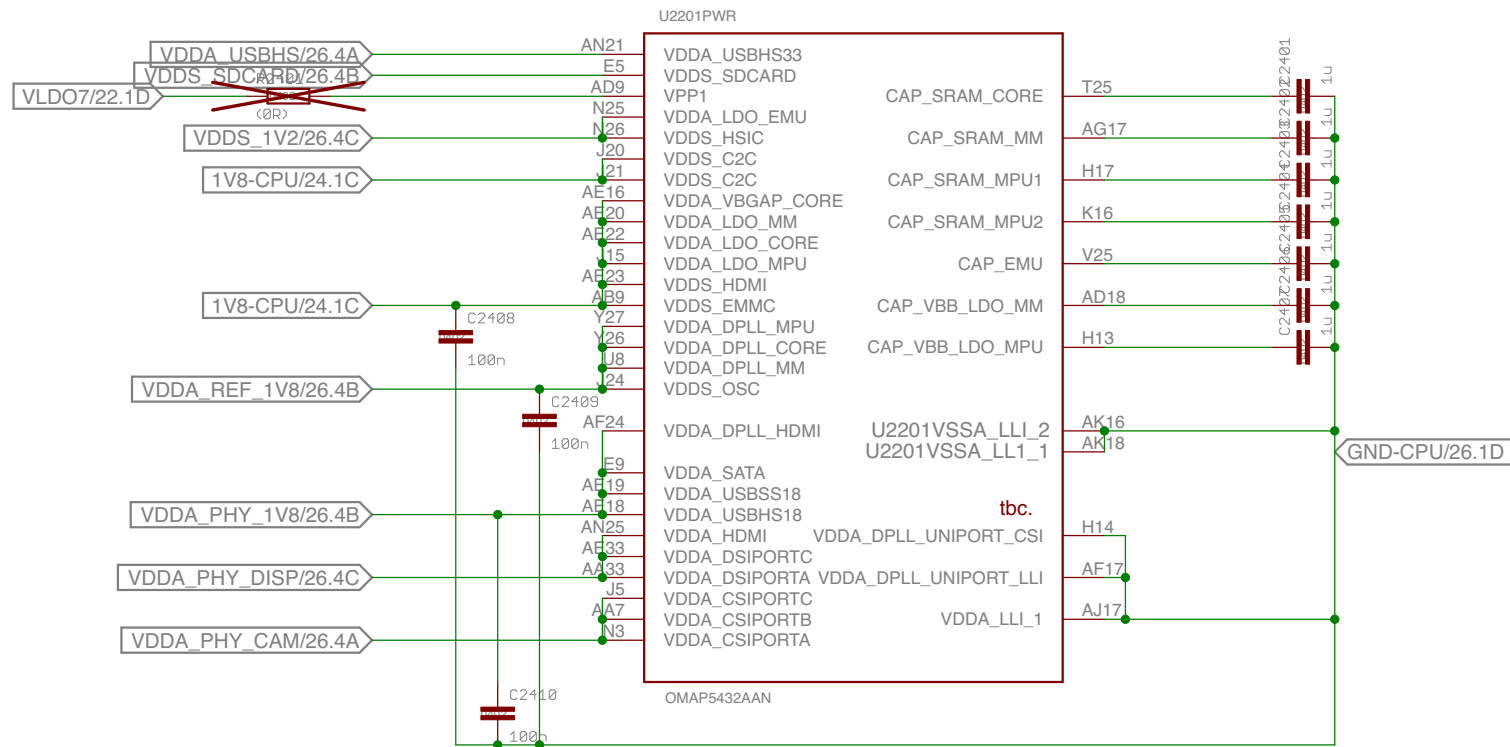
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## CPU: Misc Power

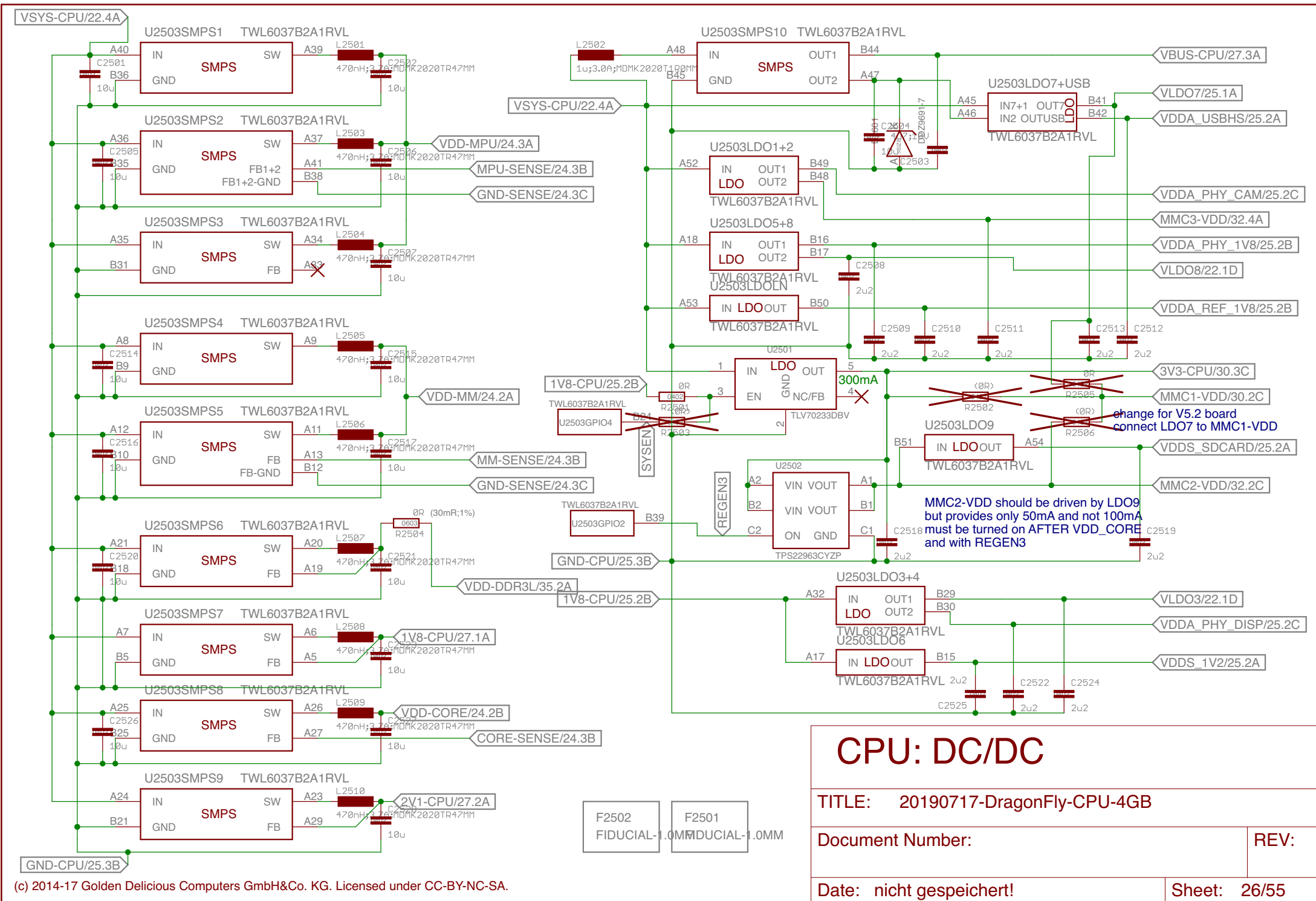
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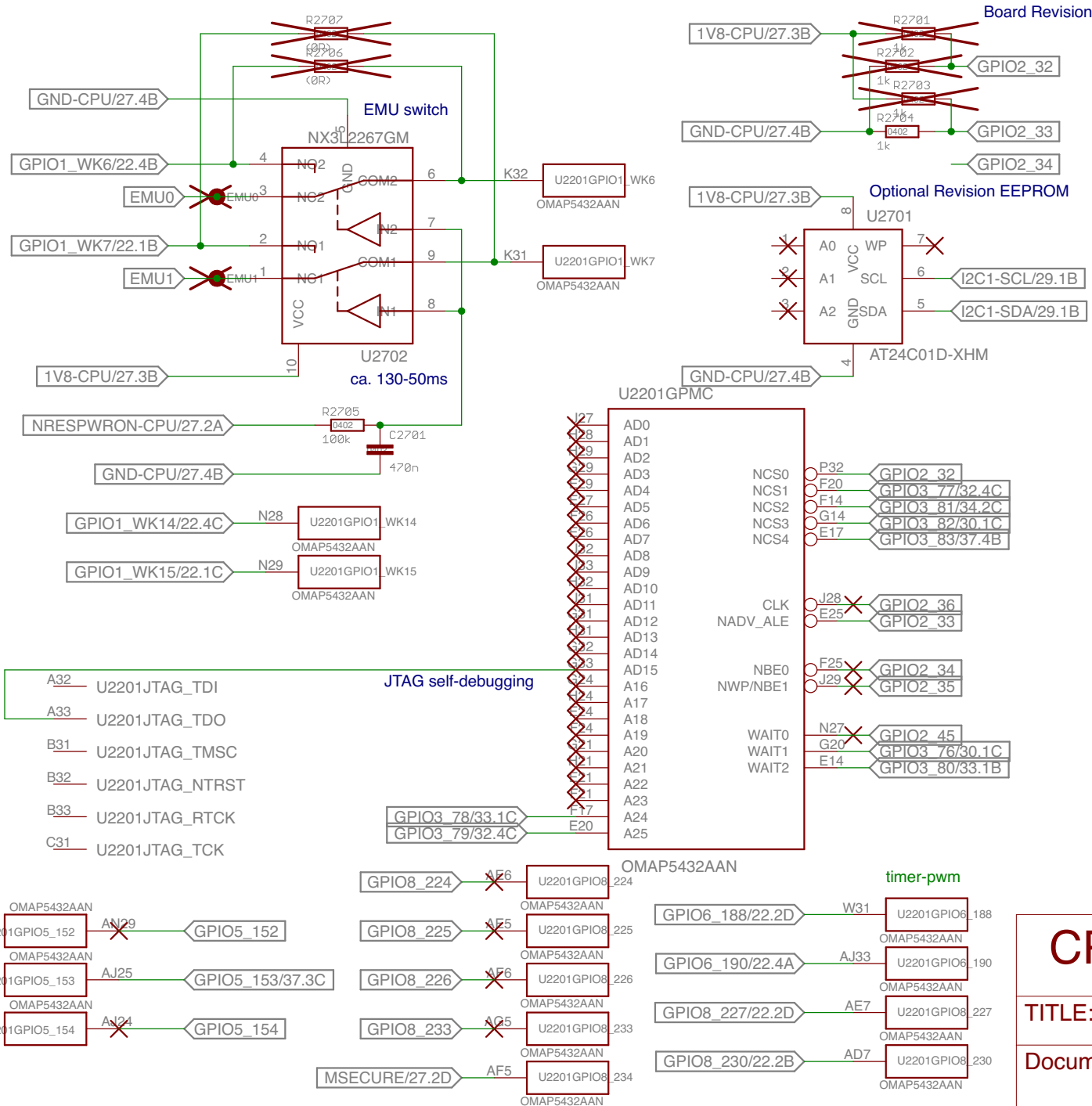
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\* CPU can also probe for unconnected pin (through PUP/PDN)  
 \* Change revision code only if change is noticeable but difficult to detect by software  
 \* There is a mismatch between version number in Stop mask and code  
 \* GPIO2\_33 encodes the memory size:  
 GPIO2\_33=PU R2703 (8GB)  
 GPIO2\_33=floating 2GB  
 GPIO2\_33=PD R2704 4GB  
 \* Revisions:  
 (4.9: no resistors)  
 (5.0: R2701 GPIO2\_32=PU + GPIO2\_33=floating)  
 5.1(.2): R2702 GPIO2\_32=PD  
 5.2(5.1.3): R2703 GPIO2\_32=PU + GPIO2\_33=PD  
 5.3: R2704 GPIO2\_33=floating  
 For a future V5.4, we can activate GPIO2\_34 with two more optional resistors

| U2201DISPC | AM18 | GPIO6_165/22.4C |
|------------|------|-----------------|
| DSS_ACBIAS | AL18 | GPIO6_164/22.4C |
| DSS_PCLK   | AL13 | GPIO6_163/22.4C |
| DSS_HSYNC  | AL12 | GPIO6_162/22.4B |
| DSS_VSYNC  | AL17 | GPIO6_166/22.4C |
| DSS_D0     | AM17 | GPIO6_167/22.2A |
| DSS_D1     | AN17 | GPIO6_168/22.2A |
| DSS_D2     | AL16 | GPIO6_169/22.1C |
| DSS_D3     | AM16 | GPIO6_170/22.1C |
| DSS_D4     | AL15 | GPIO6_171/22.1C |
| DSS_D5     | AM15 | GPIO6_172/22.1C |
| DSS_D6     | AN15 | GPIO6_173/22.4A |
| DSS_D7     | AL14 | GPIO6_174/22.1C |
| DSS_D8     | AM14 | GPIO6_175/22.3B |
| DSS_D9     | AM12 | GPIO6_176/22.4B |
| DSS_D10    | AL11 | GPIO6_177/22.2B |
| DSS_D11    | AM11 | GPIO6_178/22.3C |
| DSS_D12    | AN11 | GPIO6_179/22.1B |
| DSS_D13    | AL10 | GPIO6_180/22.1B |
| DSS_D14    | AM10 | GPIO6_181/22.1B |
| DSS_D15    | AM13 | GPIO6_161/22.2B |
| DSS_D16    | AN13 | GPIO6_160/22.2B |
| DSS_D17    | AN9  | GPIO6_182/22.2B |
| DSS_D18    | AM9  | GPIO6_183/22.4B |
| DSS_D19    | AL9  | GPIO6_184/22.4B |
| DSS_D20    | AM8  | GPIO6_185/22.4C |
| DSS_D21    | AL8  | GPIO6_186/22.1C |
| DSS_D22    | AN7  | GPIO6_187/22.3B |
| DSS_D23    |      |                 |

## CPU: GPIOs

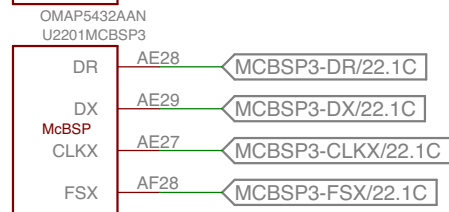
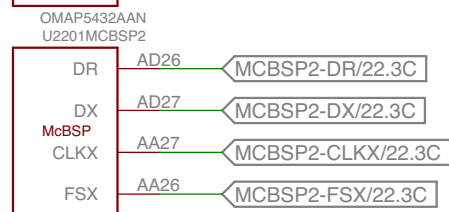
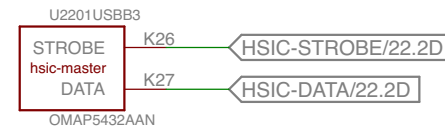
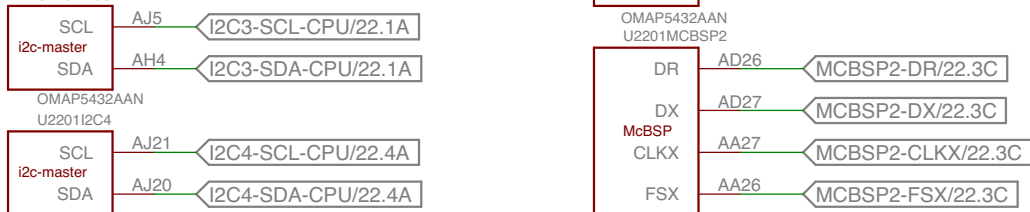
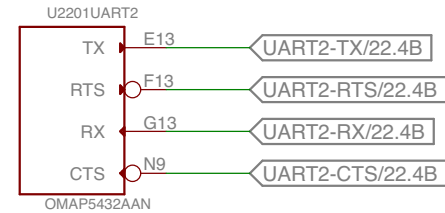
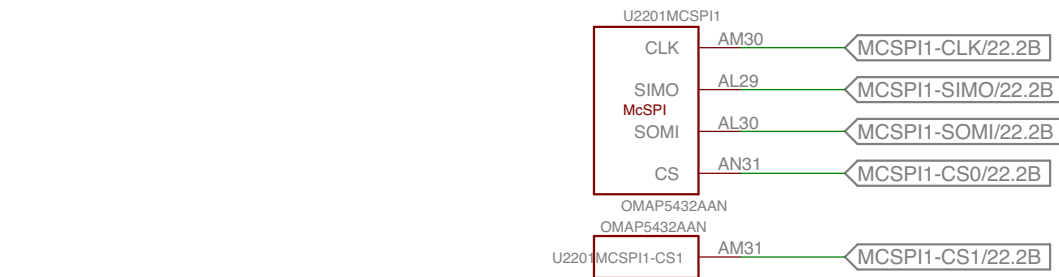
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# CPU: Serial ports

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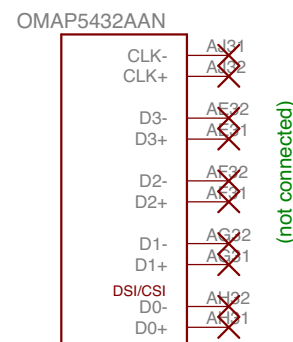
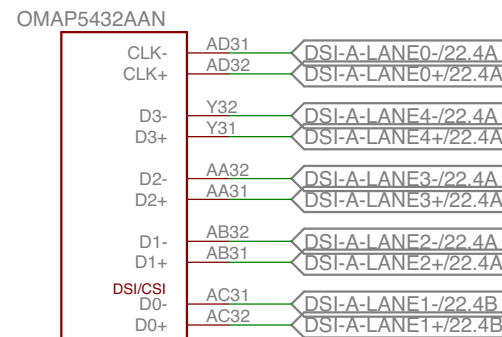
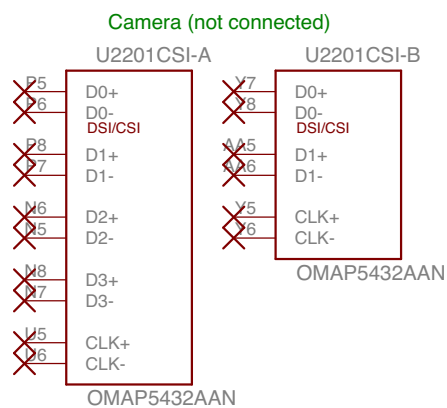
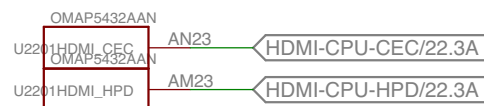
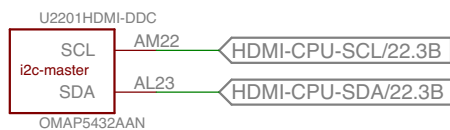
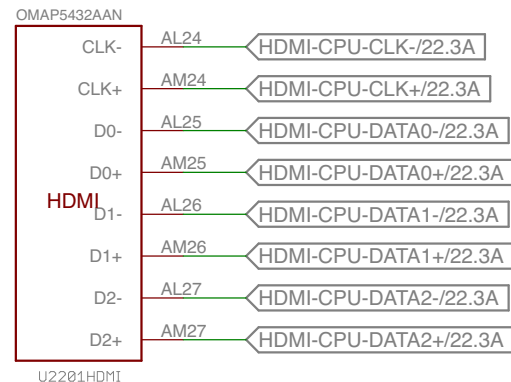
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# CPU: Display&HDMI

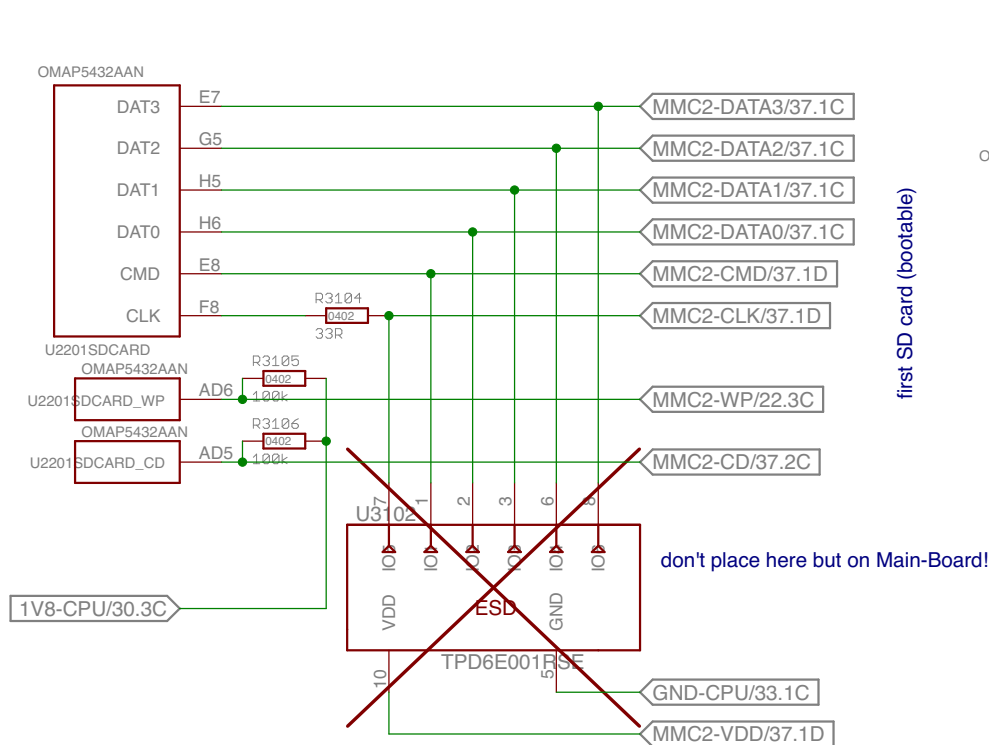
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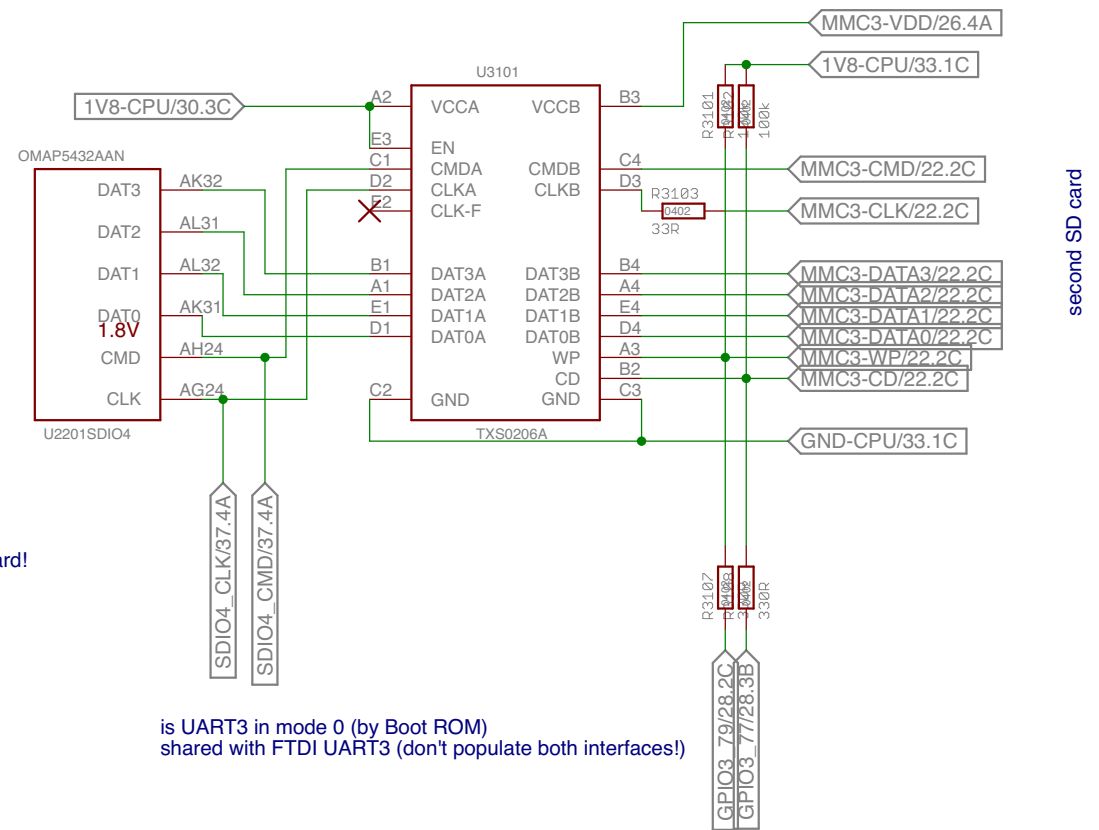
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first SD card (bootable)



second SD card



# CPU: SDIO, SD, MMC3

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